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07/14/2006

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 08/09/2006 WABDEL3 00000068 10749298

01 FC:1501 1400.00 OP
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Kuniko Alexander (Depositor's name)
 Kuniko Alexander (Signature)
 7/31/06 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/749,298	12/31/2003	Kyung-Hoon Kim	51876P560	9780

TITLE OF INVENTION: DELAY LOCKED LOOP (DLL) CIRCUIT AND METHOD FOR LOCKING CLOCK DELAY BY USING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	10/16/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
COX, CASSANDRA F	2816	327-277000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hynix Semiconductor Inc.

Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are submitted:

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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